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09/892,242

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Applicant(s)

Zheng Qi, et al.

Group Art Unit

2131

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not yet assigned

U.S. PATENT DOCUMENTS

EXAMINER INITIALS	Cite No. ¹	DOCUMENT NUMBER Number - Kind Code ² (If Known)	PUBLICATION DATE MM-DD-YYYY	NAME OF PATENTEE

FOREIGN PATENT DOCUMENTS

EXAMINER INITIALS	Cite No. ¹	Foreign Patent Document Country Code ³ - Number ⁴ - Kind Code ⁵ (If Known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T ⁶ (✓)
PP		0 661 843 A2 EPX	07-05-1995	Alvarez	
PP		EP 0 661 843 A3	11-10-1999	Alvarez	

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OTHER DOCUMENTS

EXAMINER INITIALS	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
PP		European Search Report dated September 1, 2003 from corresponding European Patent Application No. 01309324.0-1525, filed November 2, 2001
PP		JENKINS, et al.; <i>I.C. multiplexer increases analogue switching speeds</i> ; Electronic Engineering; Vol. 45, No. 540; February 1973; pp. 73-75; XP-000807191
PP		MCLOONE et al.; <i>A High Performance FPGA Implementation of DES</i> ; 2000 IEEE Workshop On Signal Processing Systems, SIPS 2000. Design and Implementation (CAT. NO. 00TH8528), October 2000; pp. 374-382 XP-002251022

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